



# The Micro/5-P CPU Board

The Micro/5-P can hold 14,000 badges. The Micro/5-P is used with **Picture Perfect™**.

Figure 1 shows the Micro/5-P board layout.

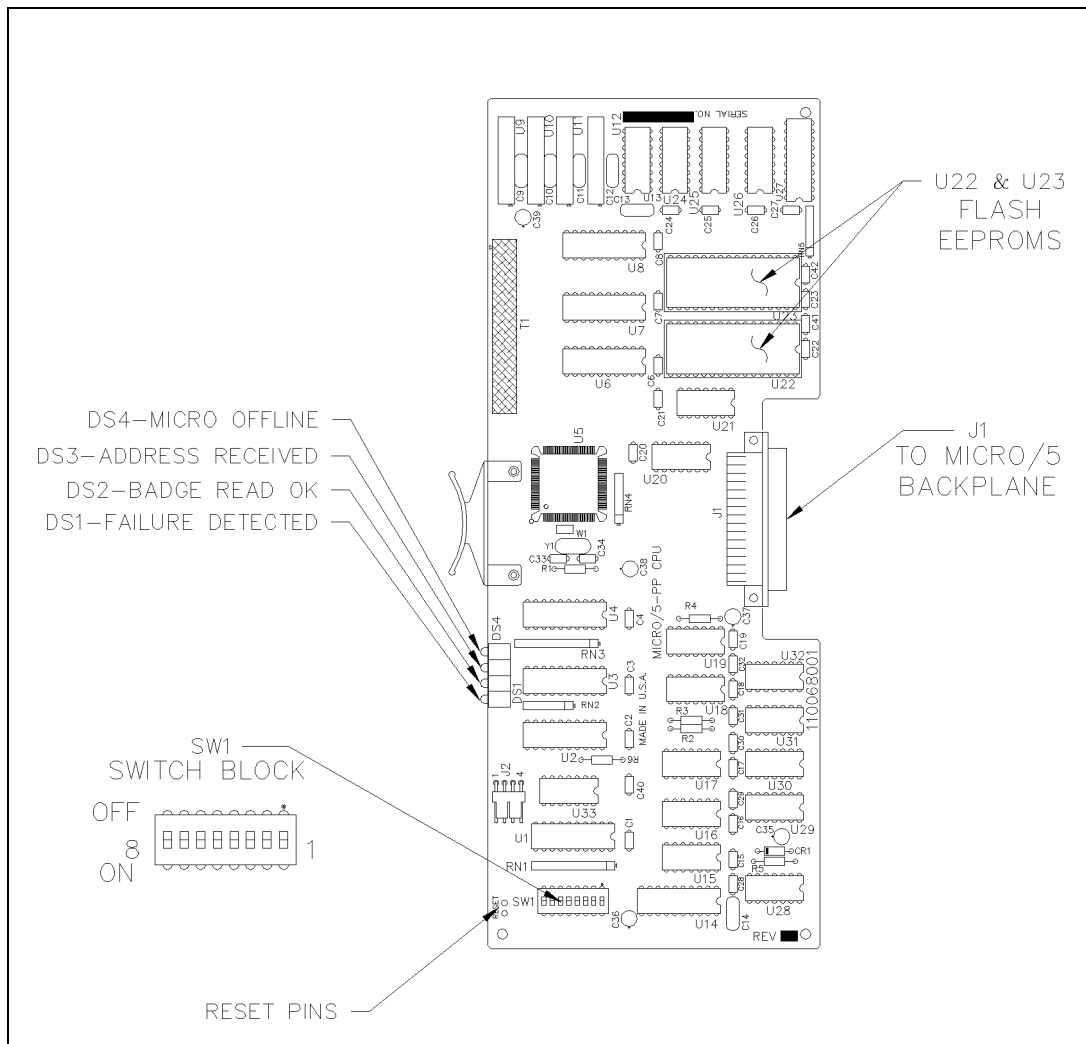


Figure 1: Micro/5-P CPU Board Layout

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## Function of the LEDs

Table 1 shows the function of the Micro/5-P CPU board LEDs. Looking at the LEDs of an installed CPU board, the top LED is DS1.

**Maintenance mode:** The Micro/5-P is in maintenance mode before any firmware is downloaded to its flash EEPROM. Refer to the Micro Configuration Utility for instructions on downloading a firmware to the EEPROM.

**Table 1: LEDs on the Micro/5-P CPU Board**

LED Number	State in Maintenance Mode	State When Application is Running
DS4	OFF	ON = Micro Offline
DS3	OFF	ON = Address Received
DS2	ON	ON = Badge Read OK
DS1	OFF	ON = CPU Failure Detected

**NOTE:** If DS2 is continuously on (lit), the micro is in maintenance mode.

## Firmware

To download firmware refer to the Micro Configuration Utility.

You will need to download firmware when:

- the micro is in maintenance mode. Refer to Table 1, “LEDs on the Micro/5-P CPU Board,” on page 2 for LEDs state when the micro is in maintenance mode.
- upgrading to a newer version of firmware.

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# DIP Switch Settings for the Micro/5-P CPU Board

Table 2 shows switch settings for addresses 0 through 252. In **Picture Perfect**, address 0 corresponds to first microcontroller.

## Address Calculation

The formula for calculating addresses is:

SW1	1	2	3	4	5	6	7	8
Switch Value	1	2	4	8	16	32	64	128

For every switch that is ON, the switch values are added together.

### For Example:

The switches for Micro address 168 are set as follows:

SW1	1	2	3	4	5	6	7	8
Switch Setting	OFF	OFF	OFF	ON	OFF	ON	OFF	ON

Using the formula:  $8 + 32 + 128 = 168$

## Micro Address Table

Table 2: Micro/5-P CPU Boards Micro Addressing

Address	SW1-							
	1	2	3	4	5	6	7	8
0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF
1	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF
2	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF
3	ON	ON	OFF	OFF	OFF	OFF	OFF	OFF
4	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF
5	ON	OFF	ON	OFF	OFF	OFF	OFF	OFF
6	OFF	ON	ON	OFF	OFF	OFF	OFF	OFF
7	ON	ON	ON	OFF	OFF	OFF	OFF	OFF
8	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF
9	ON	OFF	OFF	ON	OFF	OFF	OFF	OFF

**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

Address	SW1-							
	1	2	3	4	5	6	7	8
10	OFF	ON	OFF	ON	OFF	OFF	OFF	OFF
11	ON	ON	OFF	ON	OFF	OFF	OFF	OFF
12	OFF	OFF	ON	ON	OFF	OFF	OFF	OFF
13	ON	OFF	ON	ON	OFF	OFF	OFF	OFF
14	OFF	ON	ON	ON	OFF	OFF	OFF	OFF
15	ON	ON	ON	ON	OFF	OFF	OFF	OFF
16	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF
17	ON	OFF	OFF	OFF	ON	OFF	OFF	OFF
18	OFF	ON	OFF	OFF	ON	OFF	OFF	OFF
19	ON	ON	OFF	OFF	ON	OFF	OFF	OFF
20	OFF	OFF	ON	OFF	ON	OFF	OFF	OFF
21	ON	OFF	ON	OFF	ON	OFF	OFF	OFF
22	OFF	ON	ON	OFF	ON	OFF	OFF	OFF
23	ON	ON	ON	OFF	ON	OFF	OFF	OFF
24	OFF	OFF	OFF	ON	ON	OFF	OFF	OFF
25	ON	OFF	OFF	ON	ON	OFF	OFF	OFF
26	OFF	ON	OFF	ON	ON	OFF	OFF	OFF
27	ON	ON	OFF	ON	ON	OFF	OFF	OFF
28	OFF	OFF	ON	ON	ON	OFF	OFF	OFF
29	ON	OFF	ON	ON	ON	OFF	OFF	OFF
30	OFF	ON	ON	ON	ON	OFF	OFF	OFF
31	ON	ON	ON	ON	ON	OFF	OFF	OFF
32	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF
33	ON	OFF	OFF	OFF	OFF	ON	OFF	OFF
34	OFF	ON	OFF	OFF	OFF	ON	OFF	OFF
35	ON	ON	OFF	OFF	OFF	ON	OFF	OFF
36	OFF	OFF	ON	OFF	OFF	ON	OFF	OFF
37	ON	OFF	ON	OFF	OFF	ON	OFF	OFF
38	OFF	ON	ON	OFF	OFF	ON	OFF	OFF

**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

Address	SW1-							
	1	2	3	4	5	6	7	8
39	ON	ON	ON	OFF	OFF	ON	OFF	OFF
40	OFF	OFF	OFF	ON	OFF	ON	OFF	OFF
41	ON	OFF	OFF	ON	OFF	ON	OFF	OFF
42	OFF	ON	OFF	ON	OFF	ON	OFF	OFF
43	ON	ON	OFF	ON	OFF	ON	OFF	OFF
44	OFF	OFF	ON	ON	OFF	ON	OFF	OFF
45	ON	OFF	ON	ON	OFF	ON	OFF	OFF
46	OFF	ON	ON	ON	OFF	ON	OFF	OFF
47	ON	ON	ON	ON	OFF	ON	OFF	OFF
48	OFF	OFF	OFF	OFF	ON	ON	OFF	OFF
49	ON	OFF	OFF	OFF	ON	ON	OFF	OFF
50	OFF	ON	OFF	OFF	ON	ON	OFF	OFF
51	ON	ON	OFF	OFF	ON	ON	OFF	OFF
52	OFF	OFF	ON	OFF	ON	ON	OFF	OFF
53	ON	OFF	ON	OFF	ON	ON	OFF	OFF
54	OFF	ON	ON	OFF	ON	ON	OFF	OFF
55	ON	ON	ON	OFF	ON	ON	OFF	OFF
56	OFF	OFF	OFF	ON	ON	ON	OFF	OFF
57	ON	OFF	OFF	ON	ON	ON	OFF	OFF
58	OFF	ON	OFF	ON	ON	ON	OFF	OFF
59	ON	ON	OFF	ON	ON	ON	OFF	OFF
60	OFF	OFF	ON	ON	ON	ON	OFF	OFF
61	ON	OFF	ON	ON	ON	ON	OFF	OFF
62	OFF	ON	ON	ON	ON	ON	OFF	OFF
63	ON	ON	ON	ON	ON	ON	OFF	OFF
64	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF
65	ON	OFF	OFF	OFF	OFF	OFF	ON	OFF
66	OFF	ON	OFF	OFF	OFF	OFF	ON	OFF
67	ON	ON	OFF	OFF	OFF	OFF	ON	OFF

**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

Address	SW1-							
	1	2	3	4	5	6	7	8
68	OFF	OFF	ON	OFF	OFF	OFF	ON	OFF
69	ON	OFF	ON	OFF	OFF	OFF	ON	OFF
70	OFF	ON	ON	OFF	OFF	OFF	ON	OFF
71	ON	ON	ON	OFF	OFF	OFF	ON	OFF
72	OFF	OFF	OFF	ON	OFF	OFF	ON	OFF
73	ON	OFF	OFF	ON	OFF	OFF	ON	OFF
74	OFF	ON	OFF	ON	OFF	OFF	ON	OFF
75	ON	ON	OFF	ON	OFF	OFF	ON	OFF
76	OFF	OFF	ON	ON	OFF	OFF	ON	OFF
77	ON	OFF	ON	ON	OFF	OFF	ON	OFF
78	OFF	ON	ON	ON	OFF	OFF	ON	OFF
79	ON	ON	ON	ON	OFF	OFF	ON	OFF
80	OFF	OFF	OFF	OFF	ON	OFF	ON	OFF
81	ON	OFF	OFF	OFF	ON	OFF	ON	OFF
82	OFF	ON	OFF	OFF	ON	OFF	ON	OFF
83	ON	ON	OFF	OFF	ON	OFF	ON	OFF
84	OFF	OFF	ON	OFF	ON	OFF	ON	OFF
85	ON	OFF	ON	OFF	ON	OFF	ON	OFF
86	OFF	ON	ON	OFF	ON	OFF	ON	OFF
87	ON	ON	ON	OFF	ON	OFF	ON	OFF
88	OFF	OFF	OFF	ON	ON	OFF	ON	OFF
89	ON	OFF	OFF	ON	ON	OFF	ON	OFF
90	OFF	ON	OFF	ON	ON	OFF	ON	OFF
91	ON	ON	OFF	ON	ON	OFF	ON	OFF
92	OFF	OFF	ON	ON	ON	OFF	ON	OFF
93	ON	OFF	ON	ON	ON	OFF	ON	OFF
94	OFF	ON	ON	ON	ON	OFF	ON	OFF
95	ON	ON	ON	ON	ON	OFF	ON	OFF
96	OFF	OFF	OFF	OFF	OFF	ON	ON	OFF

**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

Address	SW1-							
	1	2	3	4	5	6	7	8
97	ON	OFF	OFF	OFF	OFF	ON	ON	OFF
98	OFF	ON	OFF	OFF	OFF	ON	ON	OFF
99	ON	ON	OFF	OFF	OFF	ON	ON	OFF
100	OFF	OFF	ON	OFF	OFF	ON	ON	OFF
101	ON	OFF	ON	OFF	OFF	ON	ON	OFF
102	OFF	ON	ON	OFF	OFF	ON	ON	OFF
103	ON	ON	ON	OFF	OFF	ON	ON	OFF
104	OFF	OFF	OFF	ON	OFF	ON	ON	OFF
105	ON	OFF	OFF	ON	OFF	ON	ON	OFF
106	OFF	ON	OFF	ON	OFF	ON	ON	OFF
107	ON	ON	OFF	ON	OFF	ON	ON	OFF
108	OFF	OFF	ON	ON	OFF	ON	ON	OFF
109	ON	OFF	ON	ON	OFF	ON	ON	OFF
110	OFF	ON	ON	ON	OFF	ON	ON	OFF
111	ON	ON	ON	ON	OFF	ON	ON	OFF
112	OFF	OFF	OFF	OFF	ON	ON	ON	OFF
113	ON	OFF	OFF	OFF	ON	ON	ON	OFF
114	OFF	ON	OFF	OFF	ON	ON	ON	OFF
115	ON	ON	OFF	OFF	ON	ON	ON	OFF
116	OFF	OFF	ON	OFF	ON	ON	ON	OFF
117	ON	OFF	ON	OFF	ON	ON	ON	OFF
118	OFF	ON	ON	OFF	ON	ON	ON	OFF
119	ON	ON	ON	OFF	ON	ON	ON	OFF
120	OFF	OFF	OFF	ON	ON	ON	ON	OFF
121	ON	OFF	OFF	ON	ON	ON	ON	OFF
122	OFF	ON	OFF	ON	ON	ON	ON	OFF
123	ON	ON	OFF	ON	ON	ON	ON	OFF
124	OFF	OFF	ON	ON	ON	ON	ON	OFF
125	ON	OFF	ON	ON	ON	ON	ON	OFF

**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

Address	SW1-							
	1	2	3	4	5	6	7	8
126	OFF	ON	ON	ON	ON	ON	ON	OFF
127	ON	ON	ON	ON	ON	ON	ON	OFF
128	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON
129	ON	OFF	OFF	OFF	OFF	OFF	OFF	ON
130	OFF	ON	OFF	OFF	OFF	OFF	OFF	ON
131	ON	ON	OFF	OFF	OFF	OFF	OFF	ON
132	OFF	OFF	ON	OFF	OFF	OFF	OFF	ON
133	ON	OFF	ON	OFF	OFF	OFF	OFF	ON
134	OFF	ON	ON	OFF	OFF	OFF	OFF	ON
135	ON	ON	ON	OFF	OFF	OFF	OFF	ON
136	OFF	OFF	OFF	ON	OFF	OFF	OFF	ON
137	ON	OFF	OFF	ON	OFF	OFF	OFF	ON
138	OFF	ON	OFF	ON	OFF	OFF	OFF	ON
139	ON	ON	OFF	ON	OFF	OFF	OFF	ON
140	OFF	OFF	ON	ON	OFF	OFF	OFF	ON
141	ON	OFF	ON	ON	OFF	OFF	OFF	ON
142	OFF	ON	ON	ON	OFF	OFF	OFF	ON
143	ON	ON	ON	ON	OFF	OFF	OFF	ON
144	OFF	OFF	OFF	OFF	ON	OFF	OFF	ON
145	ON	OFF	OFF	OFF	ON	OFF	OFF	ON
146	OFF	ON	OFF	OFF	ON	OFF	OFF	ON
147	ON	ON	OFF	OFF	ON	OFF	OFF	ON
148	OFF	OFF	ON	OFF	ON	OFF	OFF	ON
149	ON	OFF	ON	OFF	ON	OFF	OFF	ON
150	OFF	ON	ON	OFF	ON	OFF	OFF	ON
151	ON	ON	ON	OFF	ON	OFF	OFF	ON
152	OFF	OFF	OFF	ON	ON	OFF	OFF	ON
153	ON	OFF	OFF	ON	ON	OFF	OFF	ON
154	OFF	ON	OFF	ON	ON	OFF	OFF	ON



**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

Address	SW1-							
	1	2	3	4	5	6	7	8
155	ON	ON	OFF	ON	ON	OFF	OFF	ON
156	OFF	OFF	ON	ON	ON	OFF	OFF	ON
157	ON	OFF	ON	ON	ON	OFF	OFF	ON
158	OFF	ON	ON	ON	ON	OFF	OFF	ON
159	ON	ON	ON	ON	ON	OFF	OFF	ON
160	OFF	OFF	OFF	OFF	OFF	ON	OFF	ON
161	ON	OFF	OFF	OFF	OFF	ON	OFF	ON
162	OFF	ON	OFF	OFF	OFF	ON	OFF	ON
163	ON	ON	OFF	OFF	OFF	ON	OFF	ON
164	OFF	OFF	ON	OFF	OFF	ON	OFF	ON
165	ON	OFF	ON	OFF	OFF	ON	OFF	ON
166	OFF	ON	ON	OFF	OFF	ON	OFF	ON
167	ON	ON	ON	OFF	OFF	ON	OFF	ON
168	OFF	OFF	OFF	ON	OFF	ON	OFF	ON
169	ON	OFF	OFF	ON	OFF	ON	OFF	ON
170	OFF	ON	OFF	ON	OFF	ON	OFF	ON
171	ON	ON	OFF	ON	OFF	ON	OFF	ON
172	OFF	OFF	ON	ON	OFF	ON	OFF	ON
173	ON	OFF	ON	ON	OFF	ON	OFF	ON
174	OFF	ON	ON	ON	OFF	ON	OFF	ON
175	ON	ON	ON	ON	OFF	ON	OFF	ON
176	OFF	OFF	OFF	OFF	ON	ON	OFF	ON
177	ON	OFF	OFF	OFF	ON	ON	OFF	ON
178	OFF	ON	OFF	OFF	ON	ON	OFF	ON
179	ON	ON	OFF	OFF	ON	ON	OFF	ON
180	OFF	OFF	ON	OFF	ON	ON	OFF	ON
181	ON	OFF	ON	OFF	ON	ON	OFF	ON
182	OFF	ON	ON	OFF	ON	ON	OFF	ON
183	ON	ON	ON	OFF	ON	ON	OFF	ON

**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

Address	SW1-							
	1	2	3	4	5	6	7	8
184	OFF	OFF	OFF	ON	ON	ON	OFF	ON
185	ON	OFF	OFF	ON	ON	ON	OFF	ON
186	OFF	ON	OFF	ON	ON	ON	OFF	ON
187	ON	ON	OFF	ON	ON	ON	OFF	ON
188	OFF	OFF	ON	ON	ON	ON	OFF	ON
189	ON	OFF	ON	ON	ON	ON	OFF	ON
190	OFF	ON	ON	ON	ON	ON	OFF	ON
191	ON	ON	ON	ON	ON	ON	OFF	ON
192	OFF	OFF	OFF	OFF	OFF	OFF	ON	ON
193	ON	OFF	OFF	OFF	OFF	OFF	ON	ON
194	OFF	ON	OFF	OFF	OFF	OFF	ON	ON
195	ON	ON	OFF	OFF	OFF	OFF	ON	ON
196	OFF	OFF	ON	OFF	OFF	OFF	ON	ON
197	ON	OFF	ON	OFF	OFF	OFF	ON	ON
198	OFF	ON	ON	OFF	OFF	OFF	ON	ON
199	ON	ON	ON	OFF	OFF	OFF	ON	ON
200	OFF	OFF	OFF	ON	OFF	OFF	ON	ON
201	ON	OFF	OFF	ON	OFF	OFF	ON	ON
202	OFF	ON	OFF	ON	OFF	OFF	ON	ON
203	ON	ON	OFF	ON	OFF	OFF	ON	ON
204	OFF	OFF	ON	ON	OFF	OFF	ON	ON
205	ON	OFF	ON	ON	OFF	OFF	ON	ON
206	OFF	ON	ON	ON	OFF	OFF	ON	ON
207	ON	ON	ON	ON	OFF	OFF	ON	ON
208	OFF	OFF	OFF	OFF	ON	OFF	ON	ON
209	ON	OFF	OFF	OFF	ON	OFF	ON	ON
210	OFF	ON	OFF	OFF	ON	OFF	ON	ON
211	ON	ON	OFF	OFF	ON	OFF	ON	ON
212	OFF	OFF	ON	OFF	ON	OFF	ON	ON

**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

Address	SW1-							
	1	2	3	4	5	6	7	8
213	ON	OFF	ON	OFF	ON	OFF	ON	ON
214	OFF	ON	ON	OFF	ON	OFF	ON	ON
215	ON	ON	ON	OFF	ON	OFF	ON	ON
216	OFF	OFF	OFF	ON	ON	OFF	ON	ON
217	ON	OFF	OFF	ON	ON	OFF	ON	ON
218	OFF	ON	OFF	ON	ON	OFF	ON	ON
219	ON	ON	OFF	ON	ON	OFF	ON	ON
220	OFF	OFF	ON	ON	ON	OFF	ON	ON
221	ON	OFF	ON	ON	ON	OFF	ON	ON
222	OFF	ON	ON	ON	ON	OFF	ON	ON
223	ON	ON	ON	ON	ON	OFF	ON	ON
224	OFF	OFF	OFF	OFF	OFF	ON	ON	ON
225	ON	OFF	OFF	OFF	OFF	ON	ON	ON
226	OFF	ON	OFF	OFF	OFF	ON	ON	ON
227	ON	ON	OFF	OFF	OFF	ON	ON	ON
228	OFF	OFF	ON	OFF	OFF	ON	ON	ON
229	ON	OFF	ON	OFF	OFF	ON	ON	ON
230	OFF	ON	ON	OFF	OFF	ON	ON	ON
231	ON	ON	ON	OFF	OFF	ON	ON	ON
232	OFF	OFF	OFF	ON	OFF	ON	ON	ON
233	ON	OFF	OFF	ON	OFF	ON	ON	ON
234	OFF	ON	OFF	ON	OFF	ON	ON	ON
235	ON	ON	OFF	ON	OFF	ON	ON	ON
236	OFF	OFF	ON	ON	OFF	ON	ON	ON
237	ON	OFF	ON	ON	OFF	ON	ON	ON
238	OFF	ON	ON	ON	OFF	ON	ON	ON
239	ON	ON	ON	ON	OFF	ON	ON	ON
240	OFF	OFF	OFF	OFF	ON	ON	ON	ON
241	ON	OFF	OFF	OFF	ON	ON	ON	ON

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**Table 2: Micro/5-P CPU Boards Micro Addressing (Continued)**

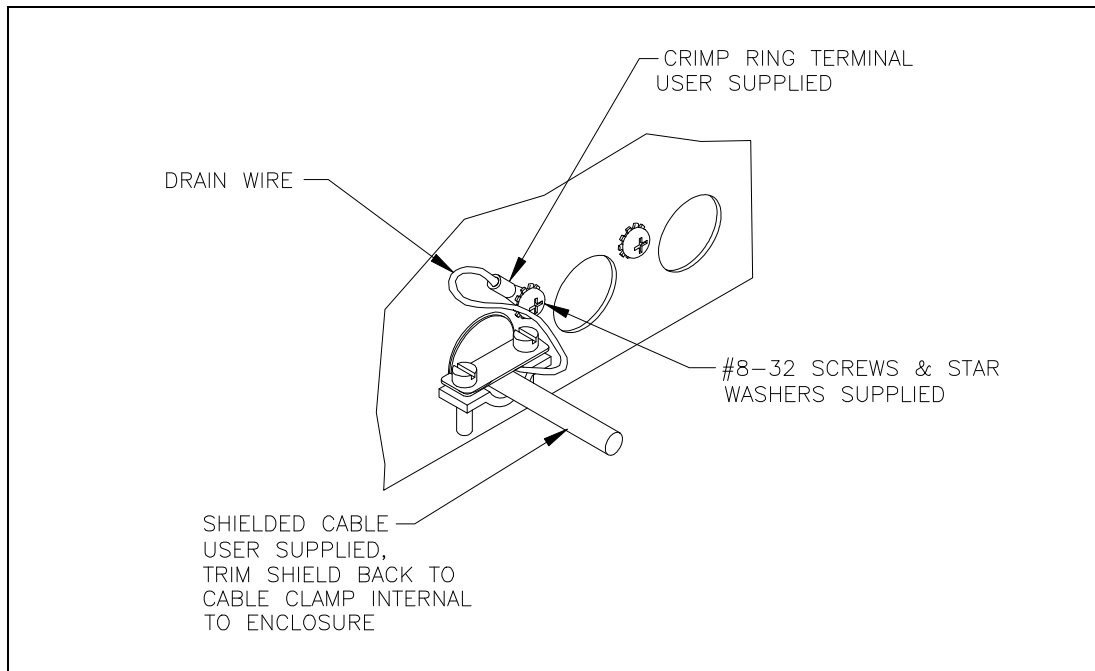
Address	SW1-							
	1	2	3	4	5	6	7	8
242	OFF	ON	OFF	OFF	ON	ON	ON	ON
243	ON	ON	OFF	OFF	ON	ON	ON	ON
244	OFF	OFF	ON	OFF	ON	ON	ON	ON
245	ON	OFF	ON	OFF	ON	ON	ON	ON
246	OFF	ON	ON	OFF	ON	ON	ON	ON
247	ON	ON	ON	OFF	ON	ON	ON	ON
248	OFF	OFF	OFF	ON	ON	ON	ON	ON
249	ON	OFF	OFF	ON	ON	ON	ON	ON
250	OFF	ON	OFF	ON	ON	ON	ON	ON
251	ON	ON	OFF	ON	ON	ON	ON	ON
252	OFF	OFF	ON	ON	ON	ON	ON	ON

## CE (European) and FCC Compliance

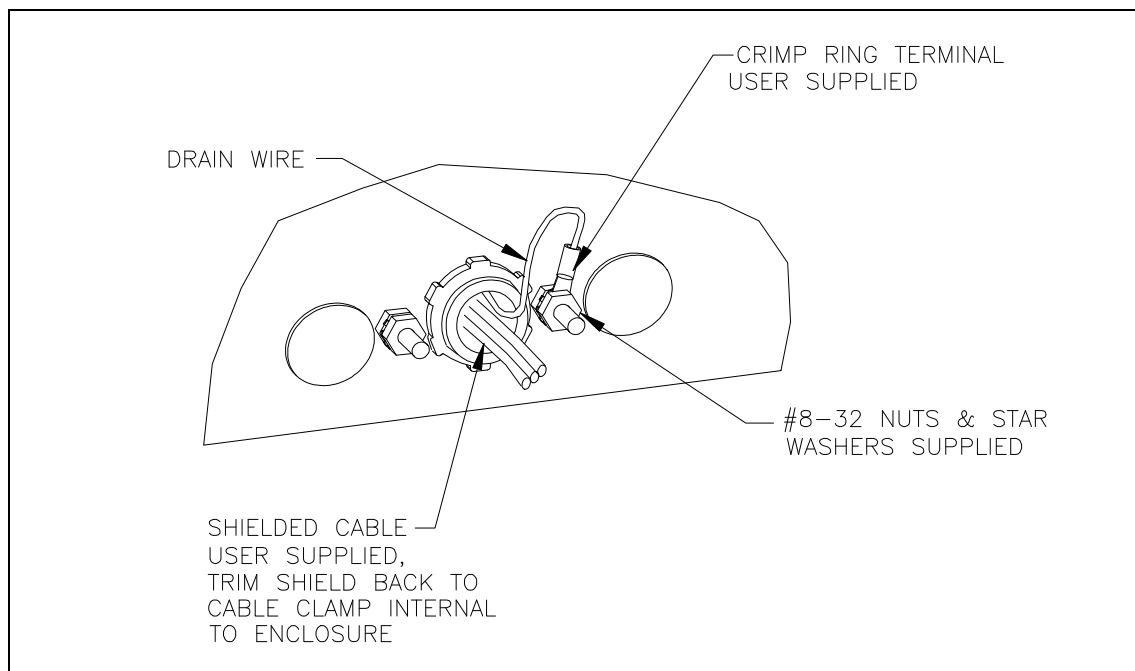
As of January 1, 1996, all new European installations **MUST** be CE compliant.

To make the Micro/5-P installation CE and FCC compliant, the following conditions must be met:

- All cables connected to the Micro/5-P must be shielded with shield terminated as shown in Figure 2 or Figure 3.
- The Power/Communications board must be in the far right slot of the Micro/5-P card cage and grounded with a factory-installed braided wire.
- The CPU board must be in the second slot from the right of the Micro/5-P card cage.
- The Micro/5-P enclosure must be connected to nearest earth ground.



**Figure 2: Typical Installation Using Shielded Cable/Drain Wire - Outside Enclosure**



**Figure 3: Typical Installation Using Shielded Cable/Drain Wire - Inside Enclosure**

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## Power/Communications Board

Table 3: Power/Communications Board – Micro Port Baud Rate

Baud Rate	Micro Port			
	SW1-3	SW1-4	SW1-5	SW1-6
1200	OFF	OFF	OFF	OFF
2400	ON	OFF	ON	OFF
4800	OFF	ON	OFF	ON
9600	ON	ON	ON	ON

When using dial-up modems, the Micro/5-P has a 2400 baud rate restriction.

## Microcontroller Wiring

The following restrictions apply to **Picture Perfect** systems only.

- If you have a Micro/5 downstream from a Micro/4, you cannot use the **Picture Perfect** flash download program to upgrade the downstream Micro/5's firmware.
- Micro/2 cannot be downstream from Micro/5-P.
- Micro/5-2 cannot be downstream from Micro/4, Micro/5-P, or Micro/5-PX.

## Reader Board Addressing

Table 4: 2RP Device Addressing - Picture Perfect (Micro/5-P)

	Board 1	Board 2	Board 3	Board 4
Readers	0 & 1	0 & 1	0 & 1	0 & 1
Door DIs	0 & 1	0 & 1	0 & 1	0 & 1
Exit DIs	8 & 9	8 & 9	8 & 9	8 & 9
Door DOs	0 & 1	0 & 1	0 & 1	0 & 1
Auxiliary/Shunt DOs	8 & 9	8 & 9	8 & 9	8 & 9

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**Table 5: 2SRP Device Addressing - Picture Perfect (Micro/5-P)**

	Board 1	Board 2	Board 3	Board 4
Readers	0 and 1	0 and 1	0 and 1	0 and 1
Door DIs	0 and 1	0 and 1	0 and 1	0 and 1
Exit DIs	8 and 9	8 and 9	8 and 9	8 and 9
Door DOs	0 and 1	0 and 1	0 and 1	0 and 1
Auxiliary/Shunt DOs	8 and 9	8 and 9	8 and 9	8 and 9

## 20 DI Board

**Table 6: DI Board Addressing**

Board #	SW1-1	SW1-2	SW1-3	SW1-4	SW1-8
1	ON	OFF	OFF	OFF	ON
2	OFF	ON	OFF	OFF	ON
3	OFF	OFF	ON	OFF	ON
4	OFF	OFF	OFF	ON	ON

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## **NOTES**

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