



PXNplus CPU Board Firmware Upgrade Instructions

Known issues resolved in build PXNP_DIST_R106

Build PXNP_DIST_R106 resolves the following issues:

- Micro crashes when loading badges with multiple unique categories. This is often exposed when loading the database from persistent memory. The micro may reboot once every hour when it is getting the database from the host.
- On occasion, the micro does not go into the normal mode when coming out of holiday mode.
- Broadcast state changes do not take effect until the micro is reset.
- When the Picture Perfect host is shut down with the micro online, the micro does not detect that it is offline with the host.
- On occasion, time synchronization of the micro with the Picture Perfect host was off by about a minute.

Check the build of your PXNplus firmware

1. In the browser Address field, enter the IP address of the micro.
2. At the Integrated Configuration Tool password screen, log in.
3. Select **Micro Info**. If the latest build is installed, the following displays:
 - Build version: PXNP_DIST_R106
 - PP version: PP_VER_114
 - SP version: SP_VER_6114

Upgrade from PXNP_DIST_R105/PXNP6113 to PXNP_DIST_R106/PXNP6114

Using the Integrated Configuration Tool:

Allocate about 5 to 7 minutes for upgrading each micro. Refer to your micro installation manual for more information about the Integrated Configuration Tool.

1. In the browser Address field, enter the IP address of the micro.
2. At the Integrated Configuration Tool password screen, log in.
3. Click **Flash Micro** and browse to the *PXNPK6114.efl* file.
The status bar on the browser should say Done and the micro should reboot automatically.
4. Log into the Integrated Configuration Tool again.
5. Click **Flash Micro** and browse to the *PXNPH6114.efl* file.
6. Wait for the file to load. The message Updated file processed correctly displays.
7. Reboot the micro manually by shorting JP6 on the PXNplus CPU board.
8. Log into the Integrated Configuration Tool again.
9. Click **Flash Micro** and browse to the *PXNP6114.efl* file.
10. Wait for the file to load. The message Updated file processed correctly displays.
11. To verify the build upgrade, refer to [Check the build of your PXNplus firmware](#) on page 1.

Using the eFlash through the Secure Perfect or Picture Perfect host:

Allocate about 20 to 25 minutes for upgrading each micro. Refer to [eFlash instructions](#) on page 3 and your host user manual for more information on eFlash.

1. eFlash the *PXNPK6114.efl* file.
2. The micro reboots automatically after the file is flashed successfully.
3. eFlash the *PXNPH6114.efl* file.
4. Reboot the micro manually by shorting JP6 on the PXNplus CPU board.
5. eFlash the *PXNP6114.efl* file.
6. To verify the build upgrade, refer to [Check the build of your PXNplus firmware](#) on page 1.

eFlash instructions

For Picture Perfect users

- When selecting the firmware `.efl` file to update a PXNplus-based micro, select both **Direct Micro File** and **Network Micro File** regardless of the actual physical communications being used. To clarify, select both **Direct Micro File** and **Network Micro File** whether you have a direct-connect, dial-up or network-based PXNplus micro.
- Because of the existing filter on the file name, the file name `PXNP6xxx.efl` does not display on the selection list. Manually enter the path to the file on Picture Perfect 2.0 and later versions.
- In order to prevent a Flash Timeout Failure (indicated by a red icon), the system administrator needs to edit the `/cas/log/.eflashrc` file and enter a minimum of these values:

```
timeout = 60
```

```
flashwait = 1440
```

Some networks may require a higher parameter value.

- When the line of micros has a combination of PXNplus and PX CPU boards, the whole line cannot be flashed at one time. When flashing PXNplus boards, select `PXNP6xxx.efl` as the **Direct Micro File**. When flashing PX boards, select `m5nxxx.hex` as the **Direct Micro File**.

For Secure Perfect users

When flashing a line of micros with a PXNplus micro as the head-of-the-line micro, only two downstream micros can be selected at a time.

